

Fig.1A

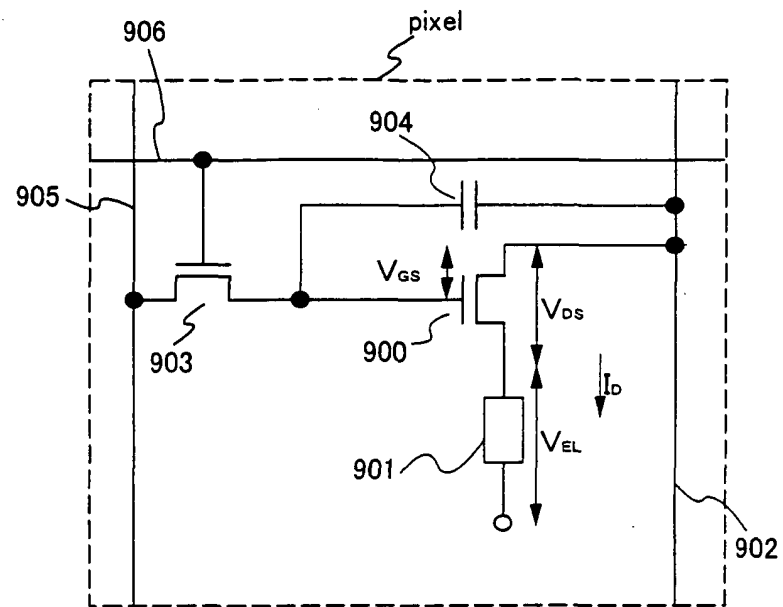


Fig.1B

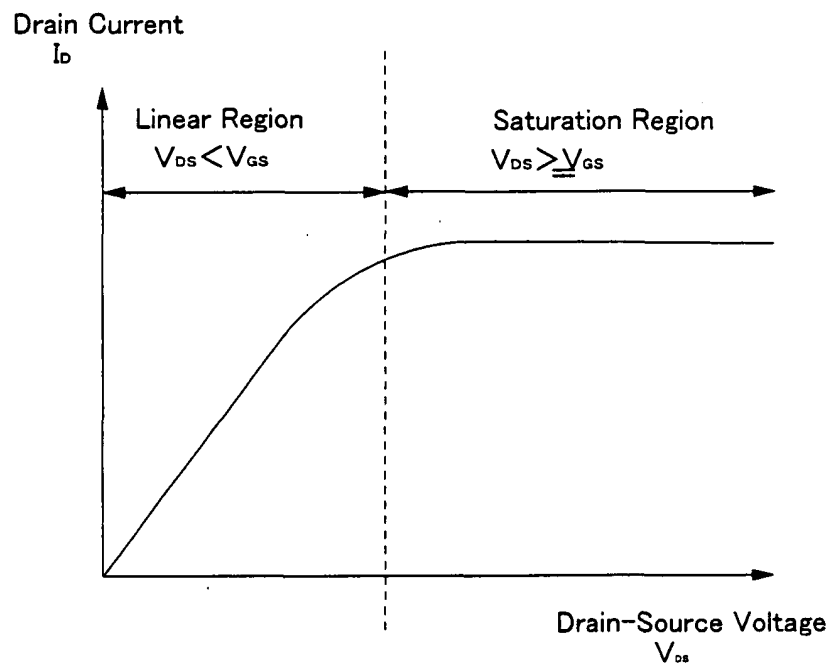


Fig.2

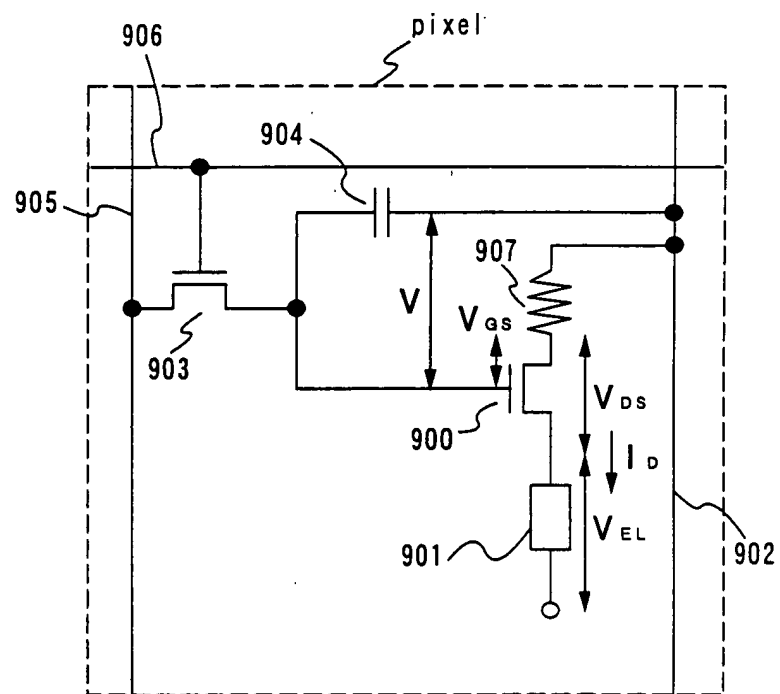


Fig.3

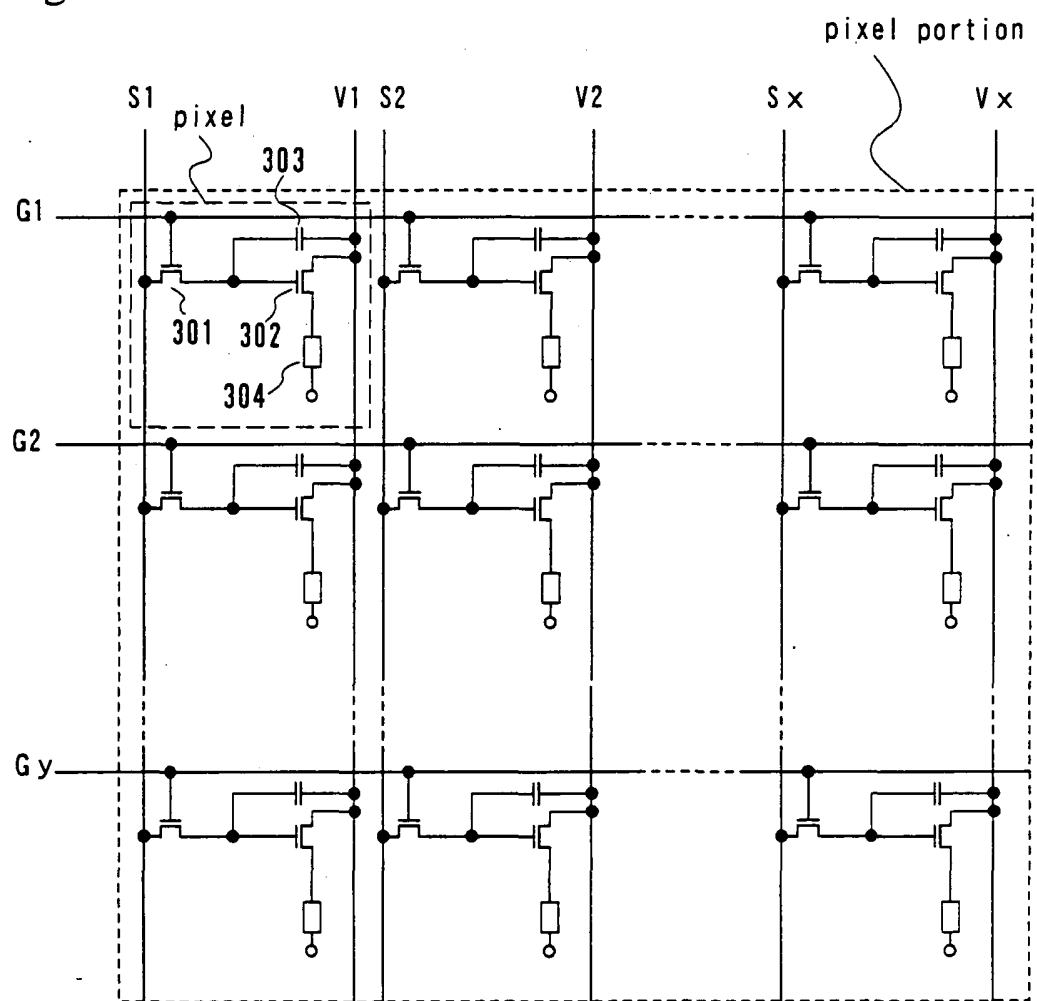


Fig.4

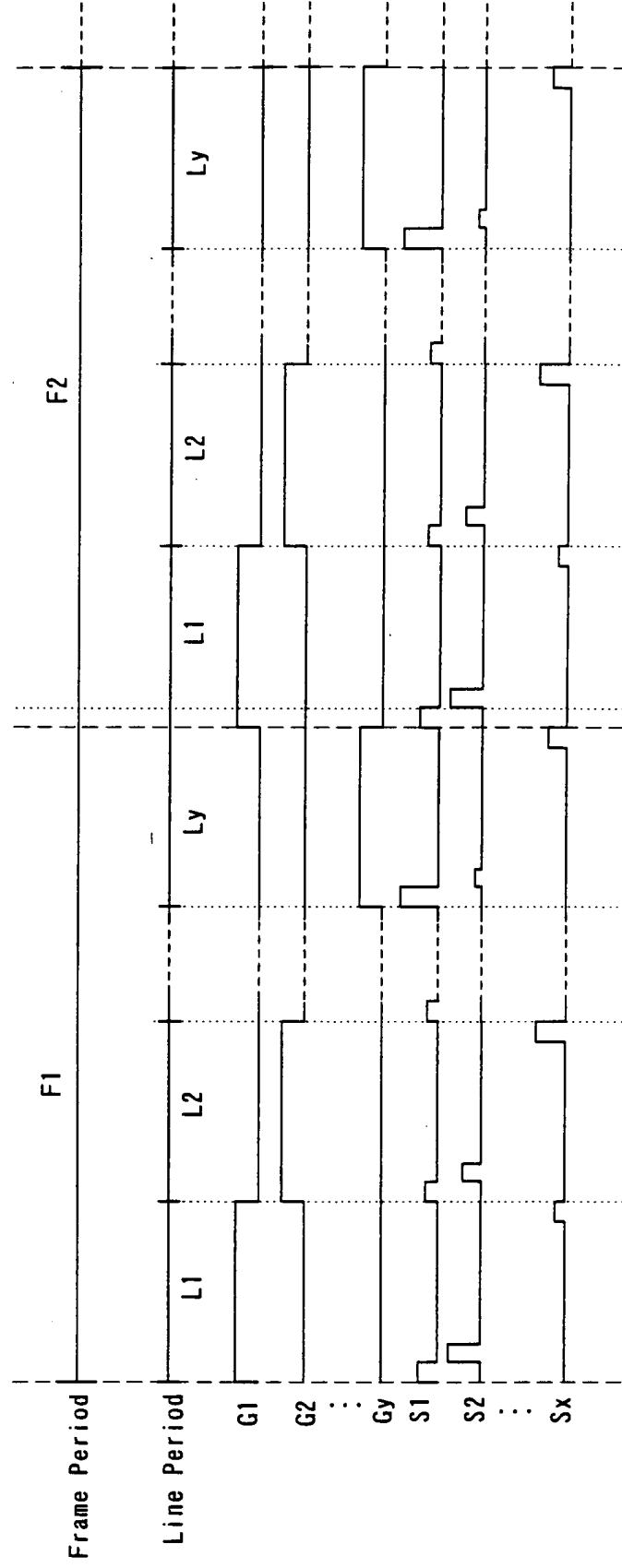


Fig.5

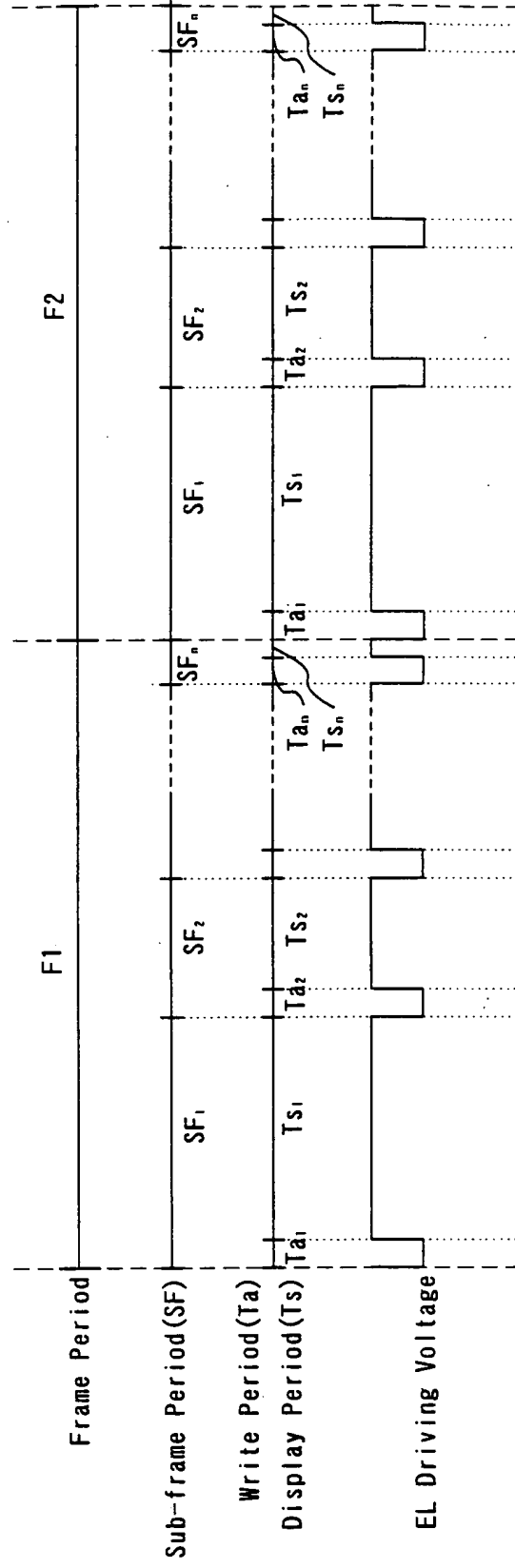


Fig.6

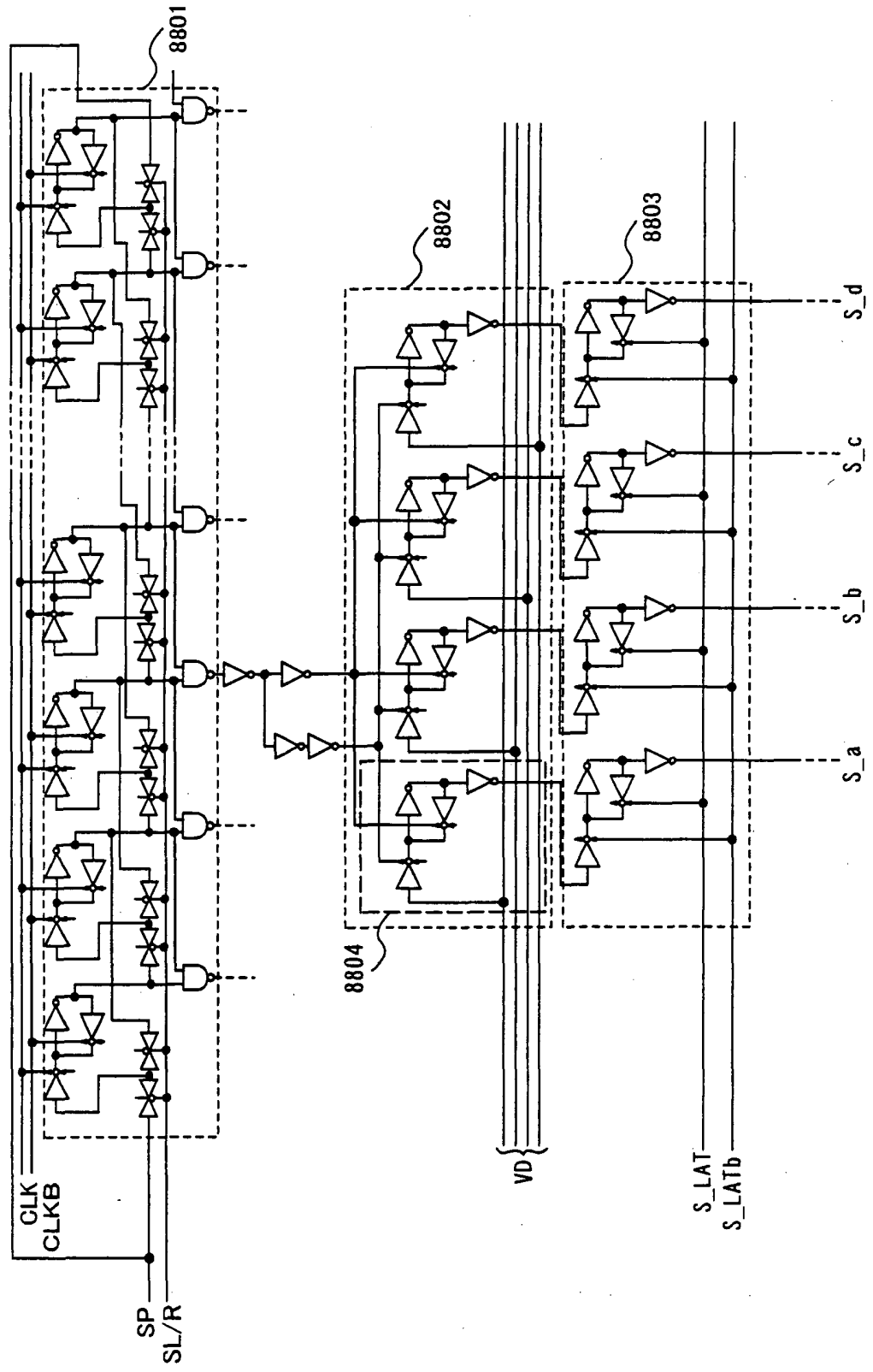


Fig.7

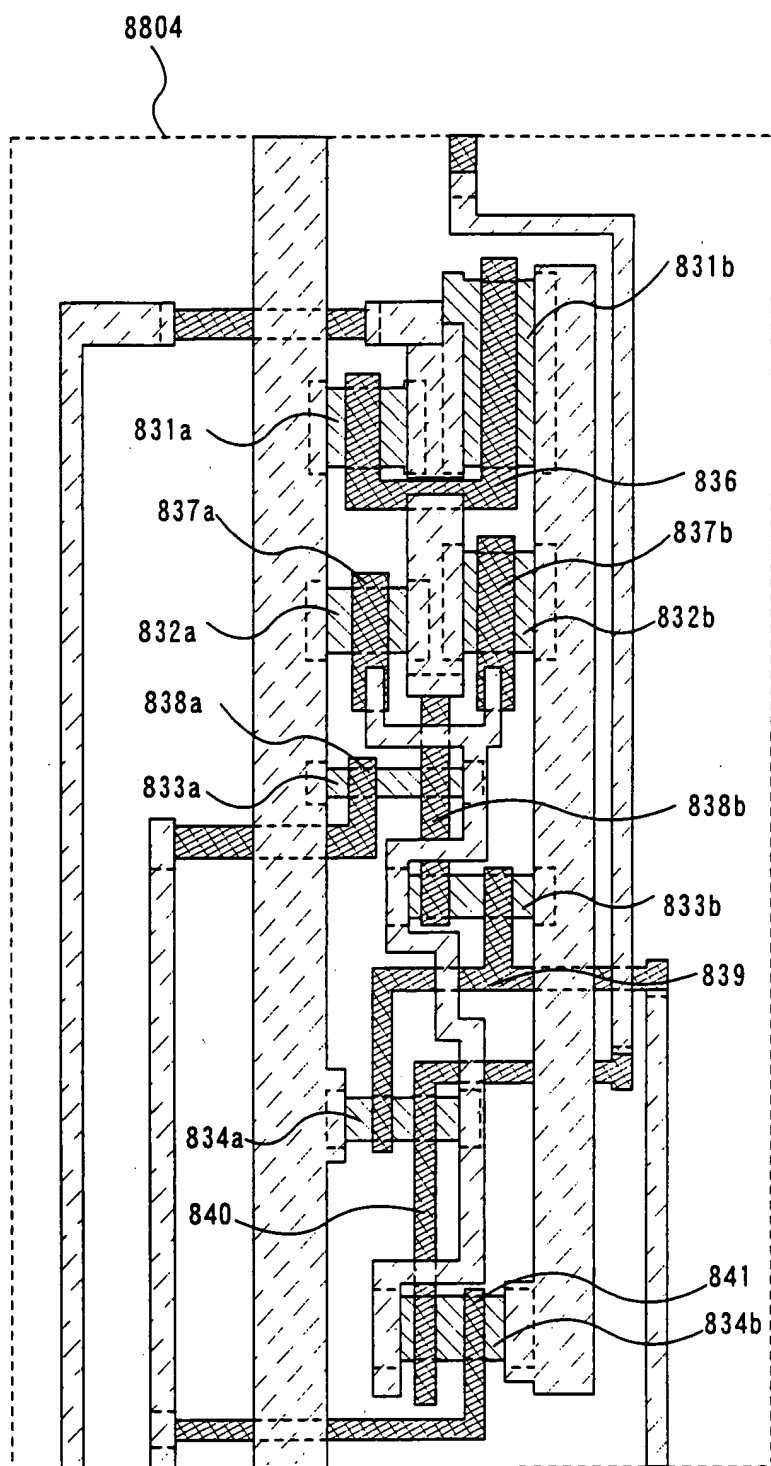


Fig. 8A

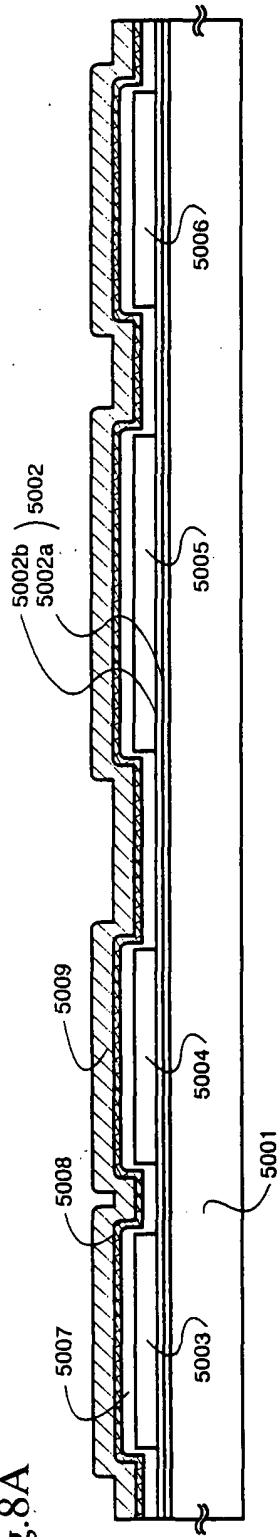


Fig. 8B

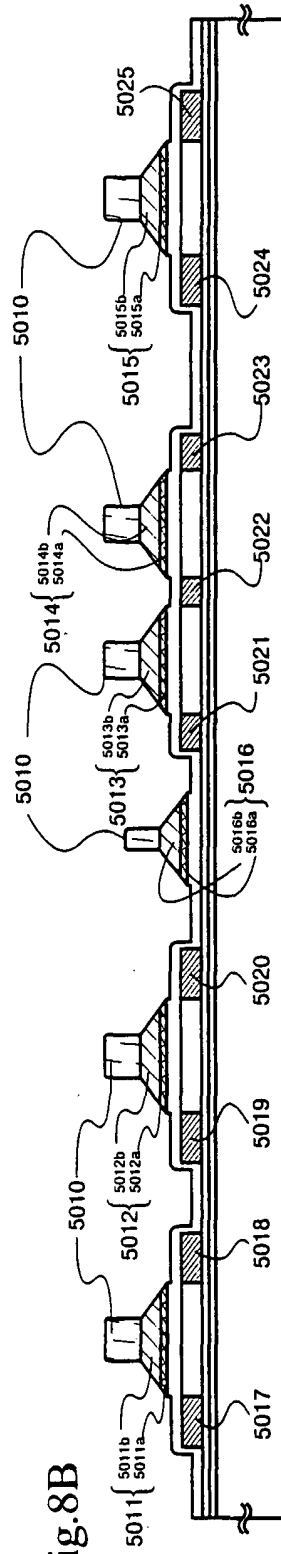


Fig. 8C

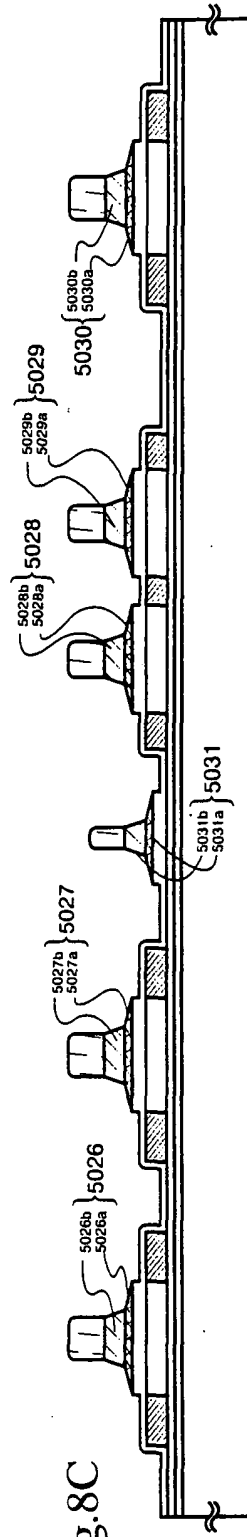


Fig.9A

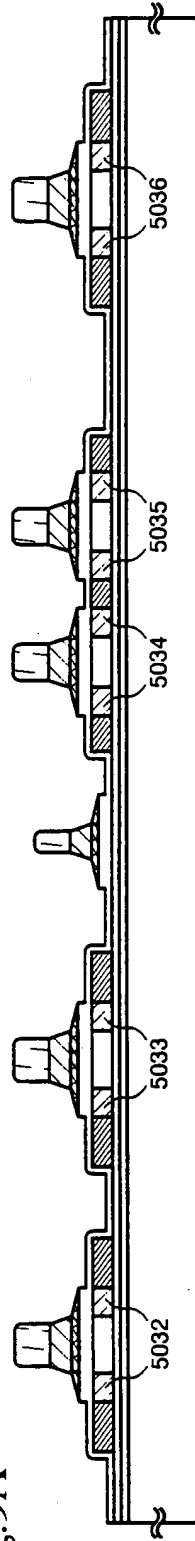


Fig.9B

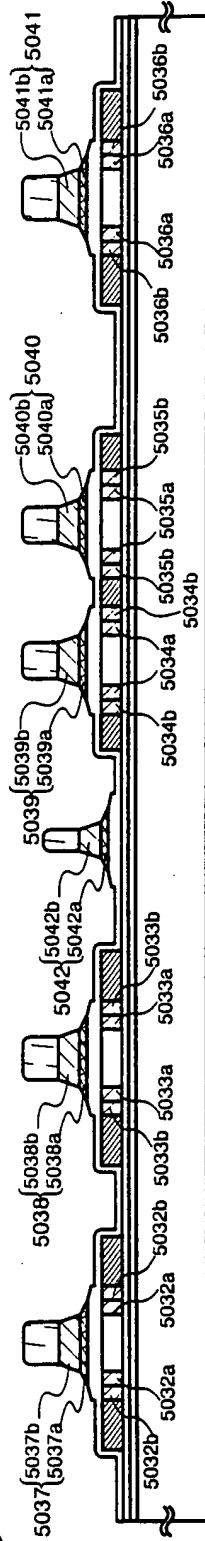


Fig.9C

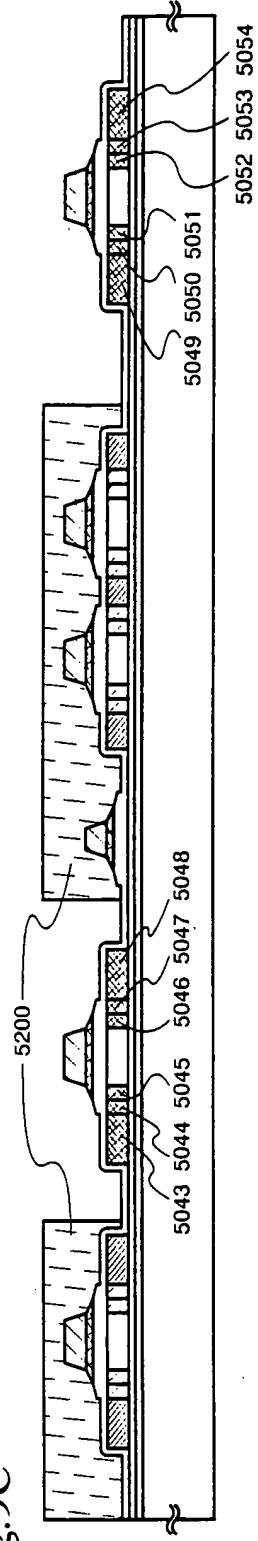
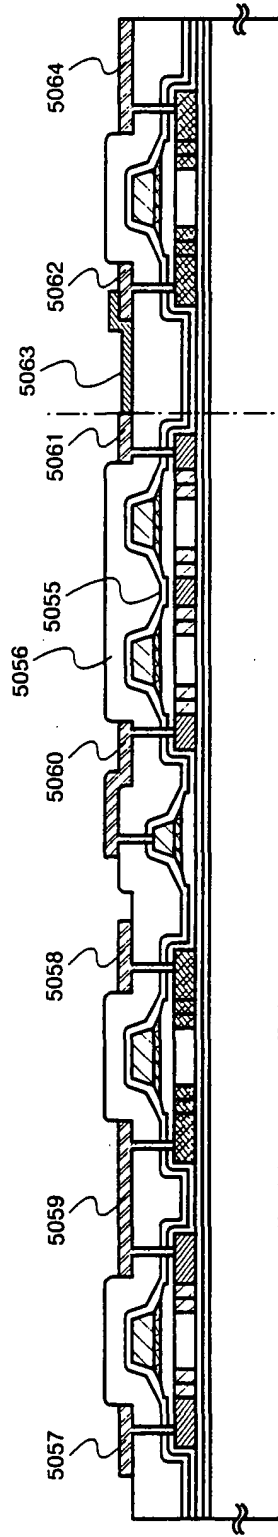


Fig.10A



driver circuit portion

5065

Fig.10B

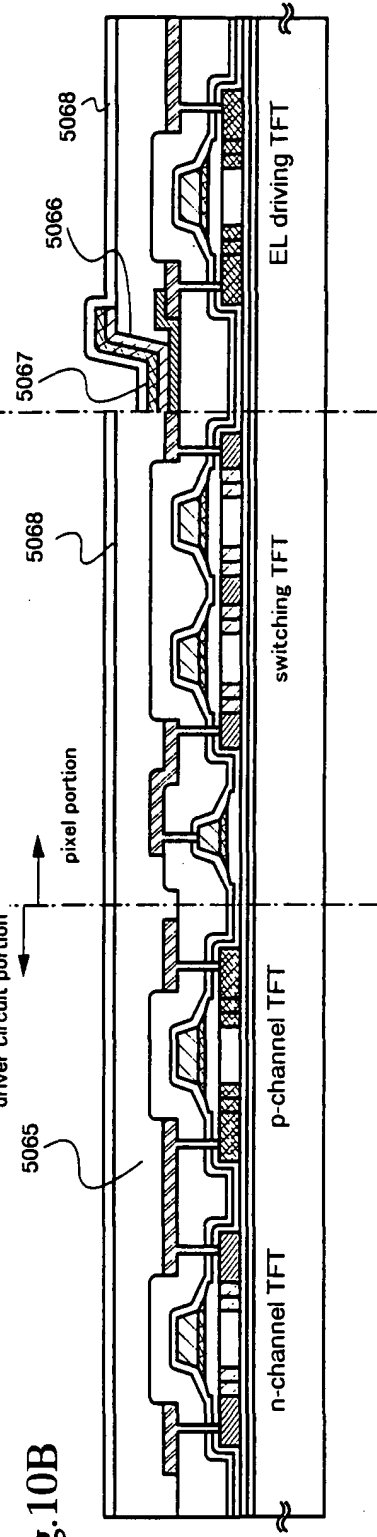


Fig.11A

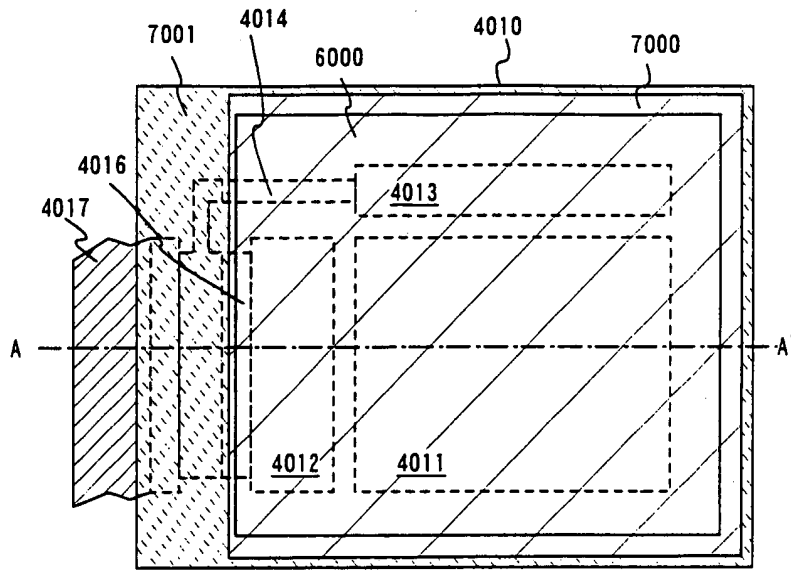


Fig.11B

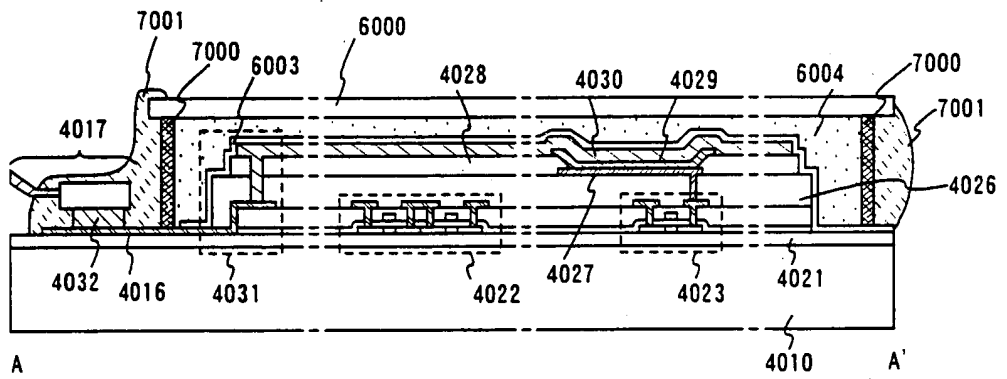


Fig.12A

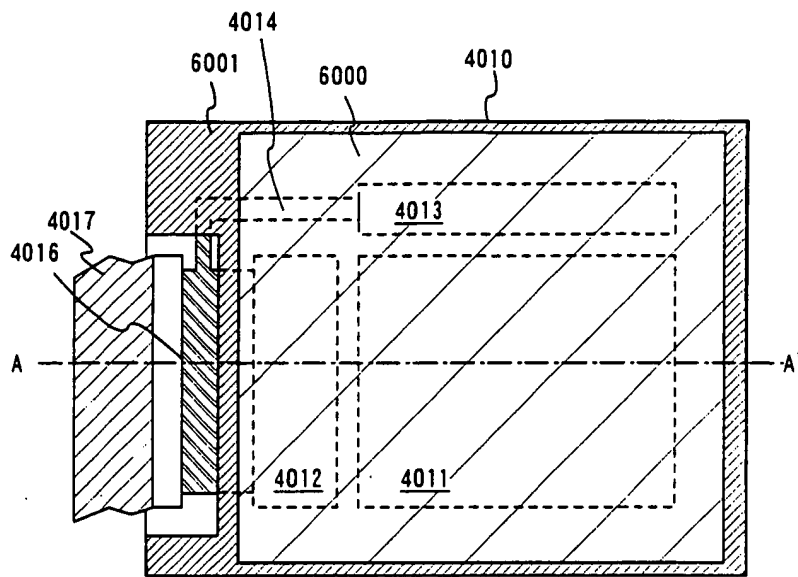


Fig.12B

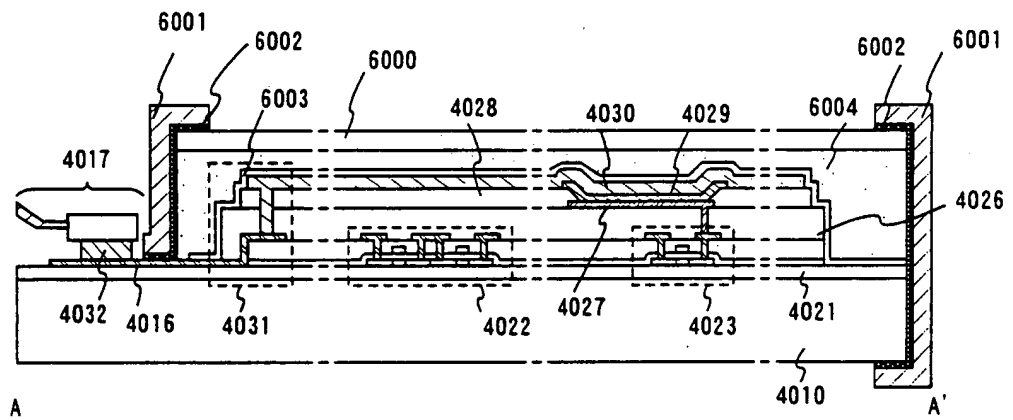


Fig.13

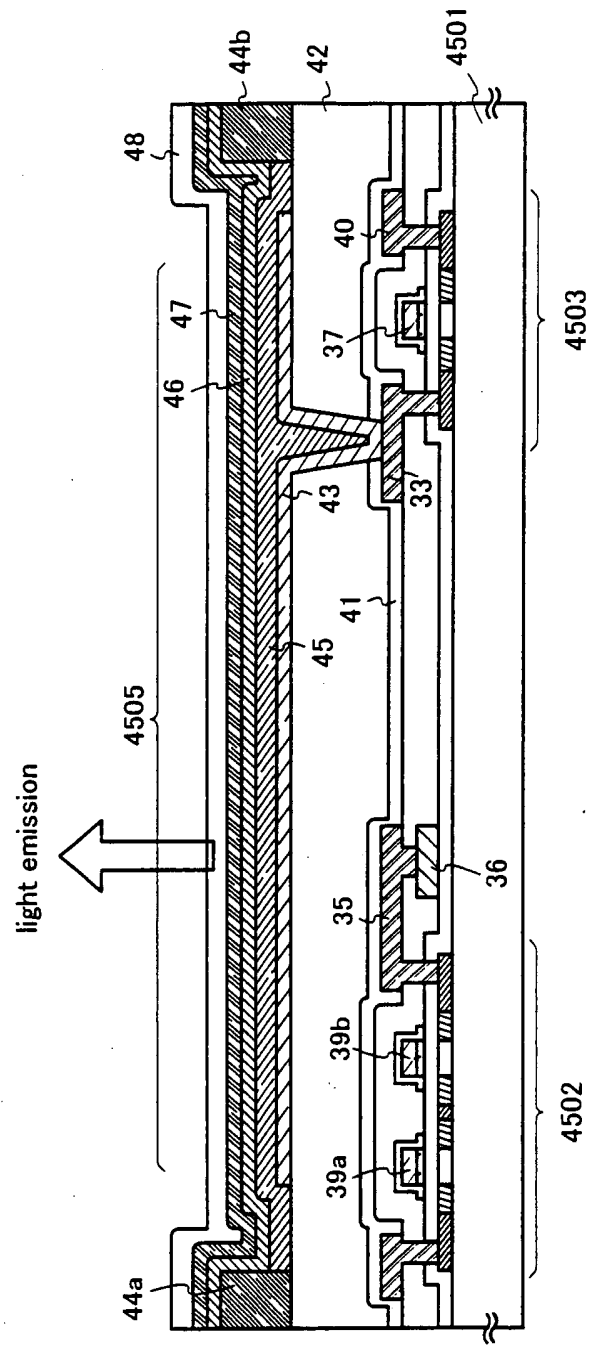
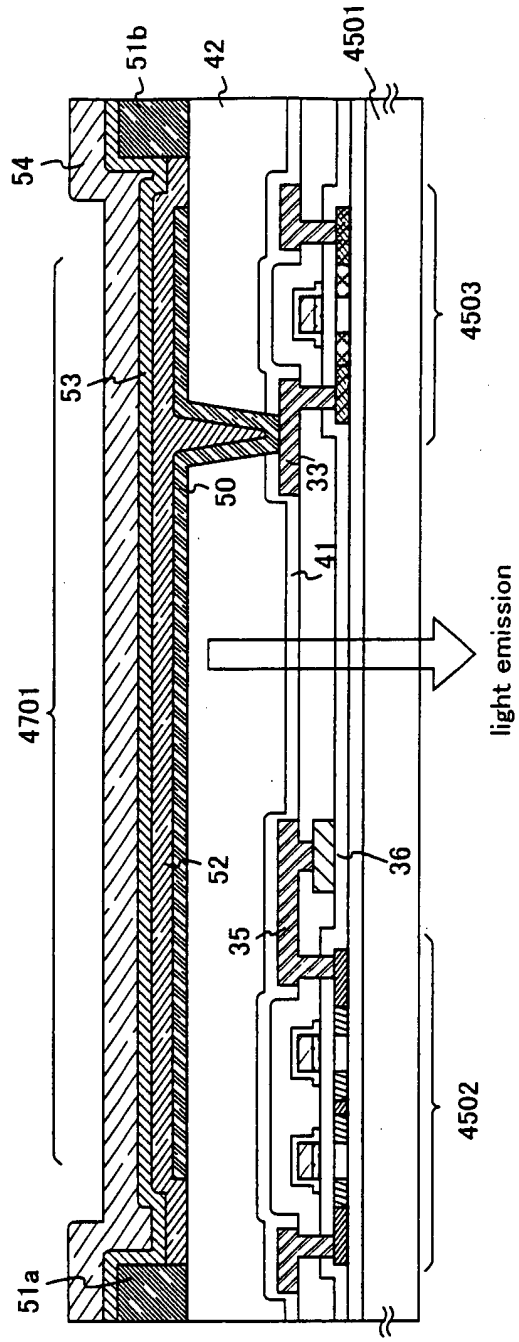


Fig.14



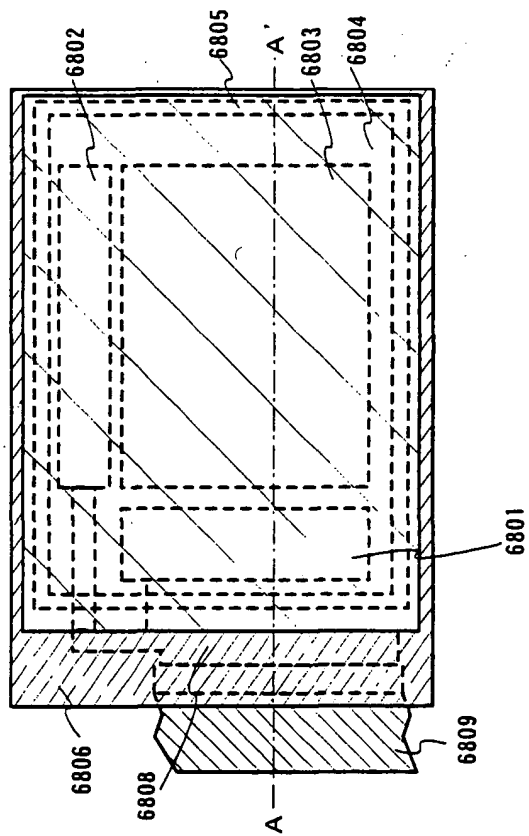


Fig.15A

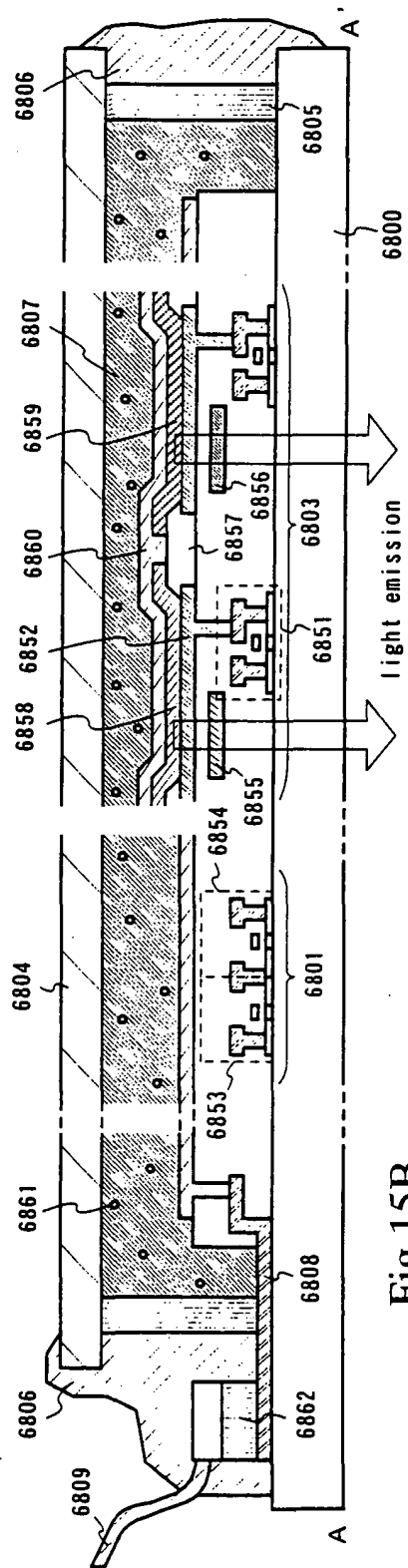


Fig. 15B

Fig.16

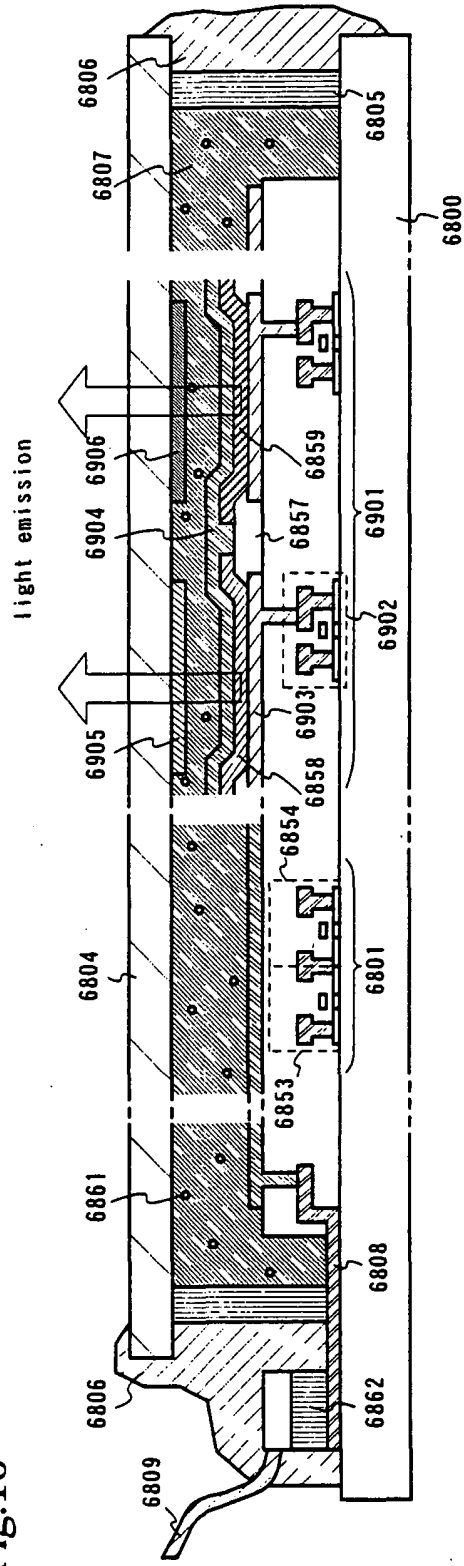
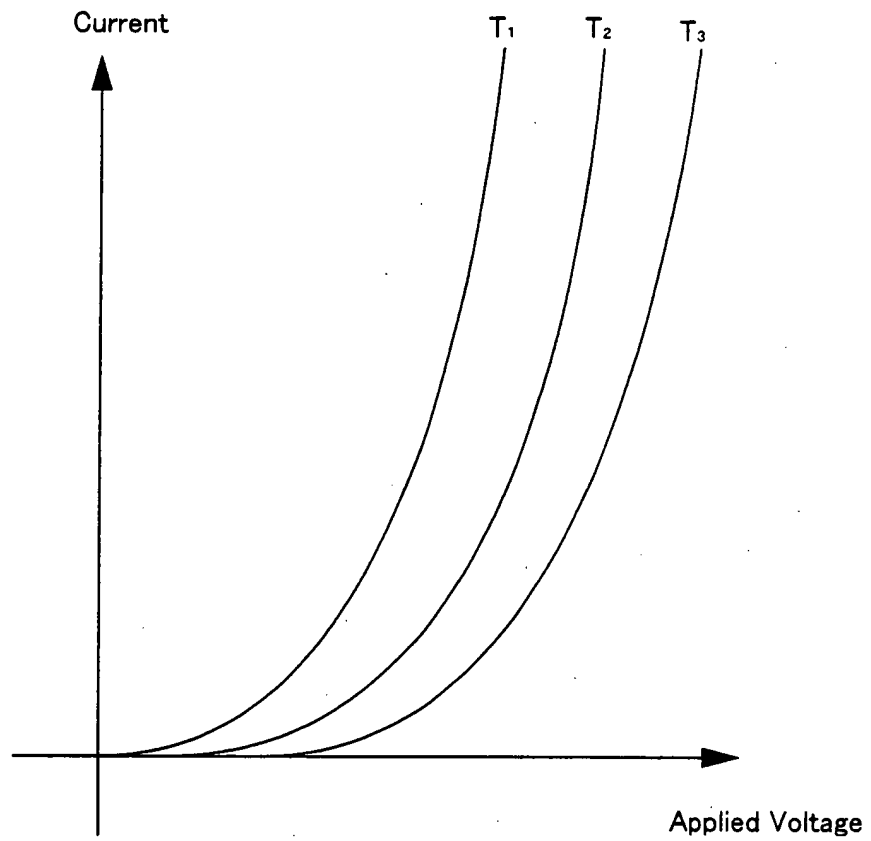


Fig.17



$$T_1 > T_2 > T_3$$

Fig.18A

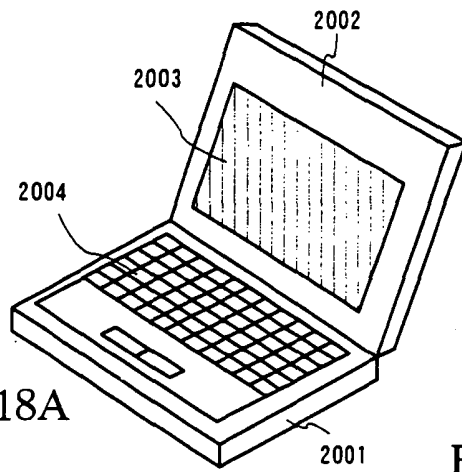


Fig.18B

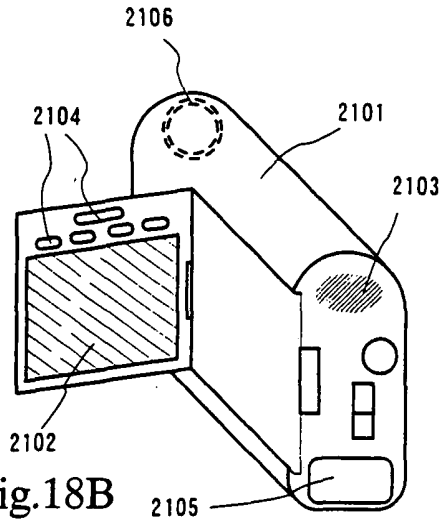


Fig.18C

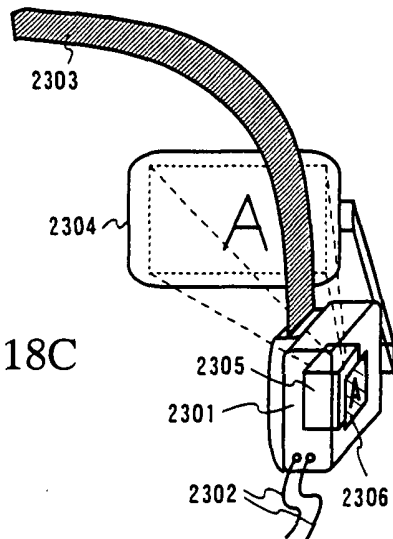


Fig.18D

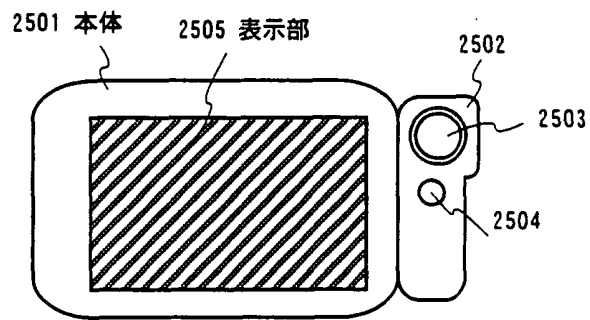
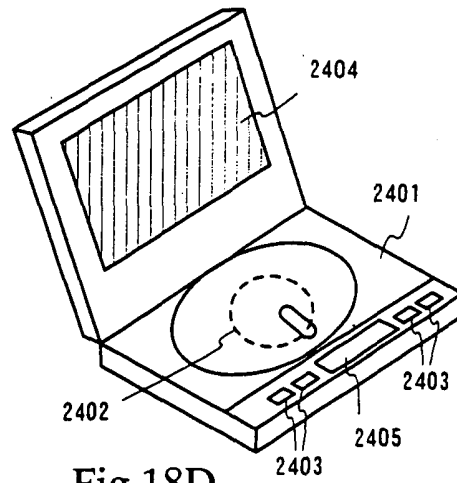


Fig.18E